

Notice of Allowability

Application No.

10/714,829

Examiner

Quoc D. Hoang

Applicant(s)

OGATA ET AL.

Art Unit

2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 1/06/2005.
2. ☒ The allowed claim(s) is/are 12-16.
3. ☒ The drawings filed on 14 November 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☒ Certified copies of the priority documents have been received in Application No. 08/911,505.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 1-2005
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


David Nelms
Supervisory Patent Examiner
Technology Center 2800

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 1/06/2005 has been entered.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on 1/06/2005 was filed after the mailing date of the Notice of Allowance on 10/01/2004. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Allowable Subject Matter

3. Claims 12-16 are allowed.

The following is an examiner's statement of reasons for allowance: None of the cited references or combinations thereof disclose or suggest the energy level is lower towards a position in which the energy level is higher matches a scan progress direction of the line-form sheet beam with respect to the semiconductor film as required by the presently claimed invention.


4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quoc Hoang whose telephone number is (571) 272-1780. The examiner can normally be reached on Monday-Friday from 8.00 AM to 5.00 PM.

If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone numbers of the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.


Quoc Hoang
Patent examiner/AU 2818


David Nelms
Supervisory Patent Examiner
Technology Center 2800

TITLE OF THE INVENTION

LASER ANNEAL METHOD OF A SEMICONDUCTOR LAYER

This application is a CON of 09/291,538 of 04/14/1999 ARN, which is a
Div of 06/911,505 Pat No. 6,274,414.

BACKGROUND OF THE INVENTION

5 FIELD OF THE INVENTION

The present invention relates to a method for manufacturing a semiconductor device, particularly to a method for manufacturing an LCD (liquid crystal display) such as a driver circuit integrated type LCD in which TFTs (thin film transistors) comprising a polycrystal semiconductor layer are formed in a display area and a driver area.

DESCRIPTION OF THE RELATED ART

In recent years, LCDs have been regularly employed in OA and AV apparatuses because of advantages resulting from their small size and thickness and their low power consumption. Active matrix type displays, in which each pixel is equipped with a TFT as a switching device for controlling the rewrite timing of image data, are especially able to display moving pictures with high resolution on a large screen, and are therefore used for displays in various televisions, personal computers, and the like.

A TFT is an FET (field effect transistor) obtained by forming a semiconductor layer together with a metal layer in a predetermined shape on an insulating substrate. In an active matrix type LCD, each TFT is connected to an electrode of each pixel capacitor formed between a pair of substrates, for